

1/5 AUS920030654US1

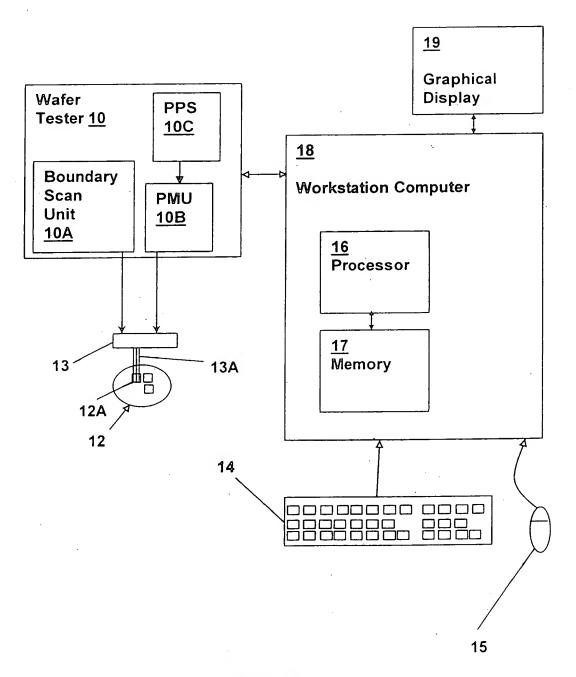


Fig. 1

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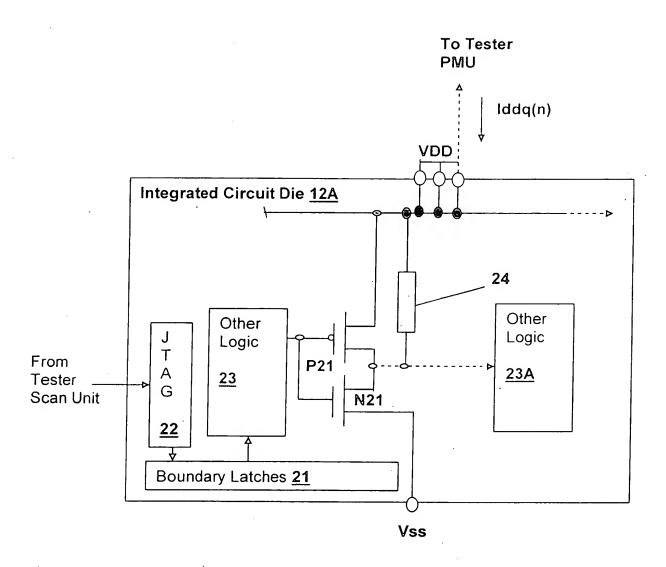
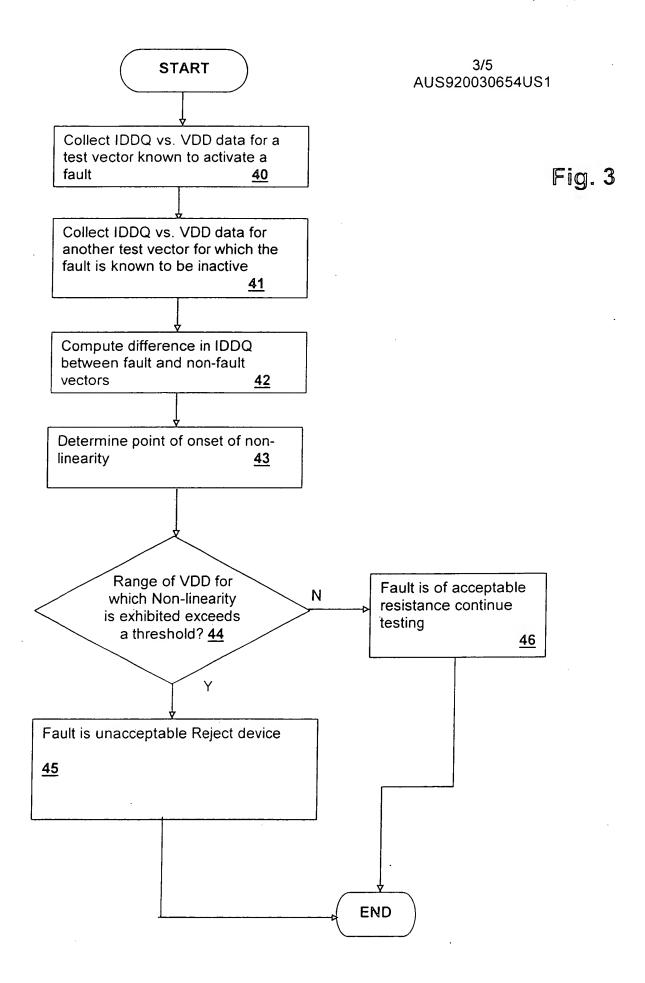


Fig. 2



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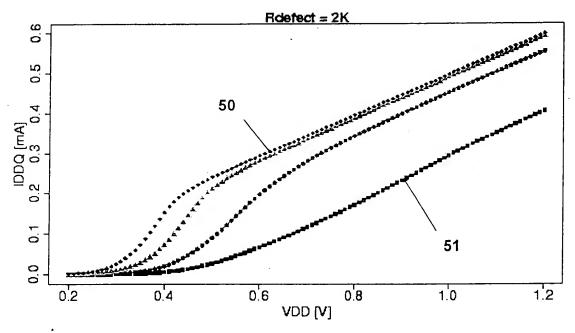


Fig. 4A

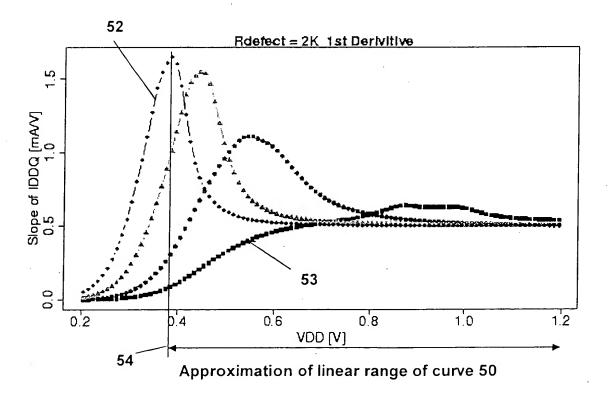


Fig. 4B

## Excess delay versus nonlinearity onset voltage

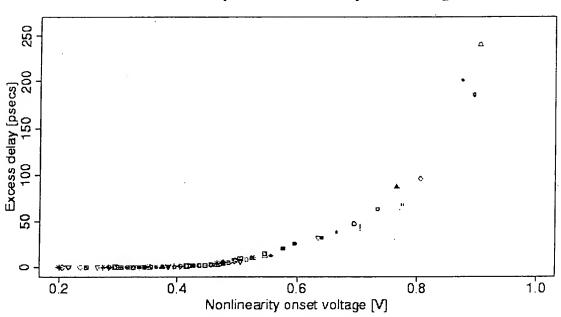


Fig. 4C